

Department of Electronic Engineering
La Trobe University
ELE3EMP – Embedded Processors

Laboratory 4
Completion Sheet

Bit Masking & Dot Matrix LEDs on the ZiLOG Evaluation Board.

There are 5 marks allocated to setup and associated questions. A further 5 marks are awarded for the programming tasks.

Family Name	Given Name	Student Number

Task	Description	Marks	Completed
Task 1	Sample code & 3 Questions	Total of 5	
Task 2	Programming Exercise	2	
Task 3	Programming Exercise	2	
Task 4	Programming Exercise	1	

Total _____ /10

John Devlin.
Revised DJS Mar 2006.
Revised GT 29 Mar 2007.