

Department of Electronic Engineering  
**La Trobe University**  
**ELE3EMP – Embedded Processors**

**Laboratory 3**  
**Completion Sheet**

Introduction to the Zilog C Development Environment and the La Trobe Keypad/LCD Development Board.

There are 5 marks allocated to setup and associated questions. A further 5 marks are awarded for the programming tasks.

Family Name	Given Name	Student Number

Task	Description	Marks	Completed
Task 1	Setup a new project in ZDS II IDE	Hurdle 0 marks	
Task 2	Questions 1.1 – 1.5	1 per question	
Task 3	Section A – parts i) & ii)	3	
Task 3	Section B – parts i) & ii)	2	

**Total**

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John Devlin.  
Revised DJS Feb 2006.