

# ELE1EDP Laboratory: Completion Sheet - 2007

## Altium Designer 6.0 — Counter: Schematic

This form **must** be handed in at the end of the laboratory session, for your group to receive **any** marks.

Computer number	Day of Week (Mon-Fri)	Date	Time

Student ID	Family Name	Given Names

**Ask a demonstrator to check your work when you have completed each Question/Task.**

Question/Task	Marker's Comments	Marker's Initials	Mark /10
1. ELE1EDP\Counter folder created			
3. Title Block			
7.1 Placed 555 timer			
7.2 Placed 4040 counter			
7.4 LED driver circuits			
7. Designators and Part Types all correct			
7.5-7.7 Power ports, Wires and Junctions			
7. Schematic complete, neat, and logically arranged			
8. Project Compiles without errors			
9. Net List generated for Protel and all Footprints correct			
Total mark / 100			

Monday 30 July 2007, Geoffrey Tobin and Adam Console.