

ELE1EDP Laboratory: Completion Sheet - 2007

Altium Designer 6.0 — PCB Layout

This form **must** be handed in at the end of the laboratory session, for you to receive **any** marks.

Computer number	Day of Week (Mon-Fri)	Date	Time

Student ID	Family Name	Given Names

Ask a demonstrator to check your work when you have completed each Question/Task.

Question/Task	Marker's Comments	Marker's Initials	Mark /10
PCB file saved as Hand-Drawn in H:\ELE1EDP\PCB\			
KeepOut boundary magenta , drawn to right dimensions .			
Components placed on Top layer.			
Components have correct footprints .			
Components placed according to example.			
Designators scaled and placed to match sample picture.			
Tracks on Bottom layer.			
Tracks have right thicknesses .			
Tracks drawn and positioned well .			
Design Rule Check has no errors.			
Total mark / 100			

Sunday 19 July 2007, Geoffrey Tobin.